

Fast Optimistically Fair Cut-and-Choose 2PC

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Abstract. Secure two party computation (2PC) is a well-studied problem with many real world applications. Due to Cleve’s result on general impossibility of fairness, however, the state-of-the-art solutions only provide security with abort. We investigate fairness for 2PC in presence of a trusted Arbiter, in an optimistic setting where the Arbiter is not involved if the parties act fairly. Existing fair solutions in this setting are by far less efficient than the fastest unfair 2PC.

We close this efficiency gap by designing protocols for fair 2PC with covert and malicious security that have competitive performance with the state-of-the-art unfair constructions. In particular, our protocols only requires the exchange of a few extra messages with sizes that only depend on the output length; the Arbiter’s load is independent of the computation size; and a malicious Arbiter can only break fairness, but not covert/malicious security even if he colludes with a party. Finally, our solutions are designed to work with the state-of-the-art optimizations applicable to garbled circuits and cut-and-choose 2PC such as free-XOR, half-gates, and the cheating-recovery paradigm.

Keywords: secure two-party computation, covert adversaries, cut-and-choose, garbled circuits, fair secure computation, optimistic fair exchange.

1 Introduction

In electronic commerce, privacy and fairness are two sought-after properties as depicted in work related to contract signing and fair exchange [5,14,49,9,6,8,21]. Fair exchange is used in electronic payments to buy or barter items [12,43] and contract signing is often used to ensure fairness: either all parties sign and agree on the contract, or the contract is invalid.

Fair secure two-party computation (2PC), a fundamental problem in cryptography, can be used to address both the privacy and fairness concerns, simultaneously. Alice and Bob would like to jointly compute a function of their private inputs, such that nothing other than the output leaks, and either both parties learn the output or either do (e.g. two banks trying to calculate a joint credit score for a customer, without giving away critical private information). Unfortunately, however, there is a significant efficiency gap between secure 2PC that achieve fairness and their unfair counterparts that have been the subject of many recent implementations and optimizations.

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2PC Without Fairness: Yao [61] introduced the first 2PC with security against honest-but-curious adversaries [46] and a large body of recent work has focused on making 2PC practical in presence of stronger (covert and malicious) adversaries [50,47,7,54,45,23].

The *cut-and-choose* paradigm is a popular method for enhancing the security of Yao’s garbled circuit protocol to the case of *malicious* (or covert) adversaries where the players can deviate arbitrarily. In a nutshell, in this paradigm, one player (Alice) garbles many circuits while the other player (Bob) checks a randomly chosen subset (to ensure that the garbling was done correctly) and evaluates the rest. Until recently, many existing solutions (e.g. [51,47,48,59,52,60]) required garbling at least $3s$ circuits to detect cheating with probability $1 - 2^{-s}$. The high number of garbled circuits is due to the fact that all these constructions ask that the evaluator computes a “majority output” and, for it to be valid, require that more than half of the evaluated circuits are correct. For the majority output to be valid, parties also need to enforce equality of the garbler’s input to the majority of the circuits evaluated. This is often handled via a procedure called **input-consistency** check.

The recent work of Lindell [45] shows how to reduce the number of garbled circuits by a factor of 3.³ In this approach, the second player evaluates the unchecked circuits, but is content with computing only one correct output (instead of a majority output) due to a **cheating-detection** component. This allows one to reduce the number of circuits to s and still achieve $1 - 2^{-s}$ security.

A more modest security guarantee for 2PC is **covert security**, proposed by Aumann and Lindell [7], which provides a practical alternative to the malicious setting. In this setting, the adversary can cheat with some small but non-negligible probability. The rationale is that a reputable real-world entity will not risk getting caught with non-negligible probability due to loss of reputation or the legal/economical costs. The protocols in the covert setting are more efficient than their malicious counterparts. For instance, in the cut-and-choose paradigm, one can settle for only garbling $s = 5$ circuits if $1 - 1/s = 4/5$ probability of getting caught is prohibitive enough. Back to our two banks computing a customer’s credit score scenario, the financial losses when a bank gets caught cheating can be seen as prohibitive as a negligible probability of cheating.

2PC with Fairness: All the above-mentioned work focus on security with abort, where the malicious party is allowed to abort the protocol *after* he learns the output of the computation, but *before* the honest party obtains the output, because it is known that achieving general fairness is impossible [20]. This limits the real world applicability of the most efficient solutions. An interested corporation is less likely to adopt 2PC solutions if it has to risk being at a competitive disadvantage by revealing the outcome of the computation to a competitor without learning it itself.

³An alternative approach for reducing the number of circuits by a factor of 1.5 was introduced by [27].

There are two main approaches for achieving fairness in general-purpose 2PC.⁴ (i) **Gradual release**-based approaches let Alice and Bob reveal each other’s output piece by piece, using super-constant rounds [55,33,58,57]. (ii) **Arbiter**-based approaches achieve constant round complexity by assuming that a trusted third party is available when needed [18,44,31,32]. **Optimistic** approaches employ the Arbiter only if there is a dispute among the parties [5].

The most relevant work to ours is that of Cachin and Camenisch [18], and the follow up work of [31], in the same optimistic Arbiter-based setting. Both constructions utilize zero-knowledge proofs that require public-key operations, and hence have a high computational cost compared to the state-of-the-art cut-and-choose 2PC. Furthermore, in [18], the Arbiter may need to redo almost the whole computation in case of a malicious behavior, which creates a bottleneck in the system.

Lindell’s optimistic framework [44], on the other hand, necessitates an electronic payment system. It is possible that one party obtains the output of the computation, whereas the other obtains a payment. [1,2,15,30,37,43] also employ such penalty-based fairness models. These constructions are incomparable to ours as they work in a different setting and make different assumptions. See Table 1 for a list of the main differences between these work and ours.

[18]	[44]	[31]
Resolutions with Arbiter take time proportional to the circuit size	Requires a payment system and employs penalty-based fairness.	Efficiently adds fairness, but to zero-knowledge based 2PC protocols only.

Table 1. Comparison to the most related previous work.

Our Contribution: In this paper we investigate fairness for 2PC in presence of a trusted Arbiter in an optimistic setting, where the Arbiter is not involved if the parties act fairly. We design efficient protocols for fair 2PC with security against covert and malicious adversaries. Our constructions follow the cut-and-choose paradigm, and for the first time, close the efficiency gap between fair 2PC and the state-of-the-art unfair solutions, in this setting. In particular:

- ✓ The overhead of our protocols against state-of-the-art unfair solutions is small; only a constant number of extra rounds and a few messages with sizes that *only* depend on the output length.
- ✓ The Arbiter’s load is minimal, and *independent* of the size of computation.
- ✓ A malicious Arbiter can *only* break fairness, but not covert/malicious security even if he colludes with a party. We prove this via a simulator for the usual security with abort definition, when the adversary is also controlling the Arbiter.
- ✓ Our protocols are *compatible* with optimizations applicable to cut-and-choose 2PC such as free-XOR [36], FleXor [35], and half-gates [62]. It also utilizes the cheating-recovery paradigm, and hence uses a reduced number of garbled circuits. These render our protocols *the most efficient* fair secure computation protocols to date.

⁴A different line of work focuses on achieving fairness not in general but for specific applications [25,3,16,22,17].

✓ Our work is the first to consider fairness in the covert adversary model.

2 Overview of Our Constructions

We review the high level ideas behind our covert and malicious 2PC constructions next, emphasizing the non-trivial parts. Our starting point in each case is the state-of-the-art protocol with security with abort (in the cut-and-choose paradigm). We then show how to enhance and modify each at very low cost in order to obtain fairness in the presence of an Arbiter.

Some of our techniques are similar to that of Kılınç and K p c  [31] who also provide an efficient solution for fair 2PC in the same setting. Similar to ours, their solution employs commitments to output labels, and verifiable escrows. But they instantiate these using zero-knowledge proofs of knowledge. In fact, verifiable escrow inherently employs zero-knowledge proofs. When one switches to the cut-and-choose setting, it is unclear how to deal with the multitude of such commitments and verifiable escrows, and still preserve correctness and efficiency. Our solutions are the first to combine optimistic Arbiter-based fairness and the cut-and-choose paradigm efficiently.

2.1 Fair Covert 2PC

There are various ways of combining fairness and covert security in a simulation-based definition. In this paper we consider the natural notion where both fairness and correctness/privacy are guaranteed with a reasonable (not all-but-negligible) probability $1 - \epsilon$ but both fairness and correctness/privacy are lost with probability ϵ against active cheating. A related notion to fairness in the covert setting is $1/p$ security [24,28]. In that line of work [26,53,11,10], the ideal world provides complete fairness (as in our case for malicious adversaries), but the simulation only needs to achieve $1/p$ indistinguishability between the ideal and real worlds. Our approach is slightly different: we directly take the covert adversary model [7,4], and modify it to preserve fairness unless the adversary cheats and remains undetected. Note that the $1/p$ security does not explicitly model detection of the adversary’s misbehavior. It is an interesting question to understand the relation between the two notions. Next, we review the main technical difficulties in our covert construction.

Security with Abort. Recall the covert 2PC protocol of Aumann and Lindell [7]. Alice generates s garbled circuits $\text{GC}_1, \dots, \text{GC}_s$. Then, the parties perform ℓ (number of input bits) oblivious transfers (OTs) for Bob to learn his garbled inputs (this is intentionally done for all s circuits and before the opening). Alice sends the s garbled circuits to Bob. Parties then perform a coin-toss to choose a random index $e \in \{1, \dots, s\}$. Alice opens the secrets for all garbled circuits and OTs except for the e^{th} one. Bob checks correctness of the opened circuits and the corresponding OTs, and aborts if cheating is detected. Else, Alice sends her garbled inputs for the e^{th} circuit. Bob evaluates the circuit and learns his own output. He also obtains the garbled values for Alice’s output, which he sends to her for translation.

It is easy to see that the above construction is not fair. We now highlight the main changes we make to this protocol to achieve fairness.

Delay Evaluator’s Output Translation. Note that Bob can abort the protocol immediately after learning his output and without forwarding Alice’s output to her. Therefore, we modify the protocol so that Alice does *not* send to Bob the translation table for his output (mapping output labels to actual bits) *until* he sends Alice’s garbled output to her. But note that this trivial change fails since now Alice can abort before sending the translation table to Bob.

Hence, we need to ensure that if Alice aborts at this stage, Bob has enough information to invoke an output resolution protocol with the Arbiter and show evidence that he has been following the steps of the protocol and hence deserves to know the output. After checking Bob’s claim, the Arbiter should be able to provide him with sufficient information to decode his output.

Prove Bob’s Honesty to the Arbiter. Notice that efficiently proving this is a non-trivial task. For example, in [18], the Arbiter and the resolving party re-perform almost the whole computation for this purpose. In our case, Bob’s proof of following through with the protocol will be the garbled output he computes for Alice’s output. Note that due to the *output-authenticity* property of the garbling scheme, Bob cannot forge this value except if he honestly computes the output. In order to enable the Arbiter to check the validity of Bob’s claimed output label, Alice will send hashes of her output labels (in permuted order) to Bob along with the garbled circuits, and a signature for the e^{th} one. Bob verifies validity of these hashes for the opened circuits. Now when he goes to the Arbiter, he shows both the output labels he obtained for Alice’s output, and the signed hashes for the e^{th} circuit. The Arbiter can verify that the two are consistent, by ensuring that there is one output label provided per pair.

Equip the Arbiter with the Translation Table for Bob’s Output. Furthermore, the Arbiter should have sufficient information to pass along to Bob for decoding his output. Hence, Alice encrypts the translation table for Bob’s output under the Arbiter’s public key and sends it to Bob along with the garbled circuits, and a signature for the e^{th} one. Bob checks validity of these encryptions for the opened circuits. Once Bob’s claim of behaving honestly is verified, the Arbiter can decrypt the translation table, and send it to Bob for him to decode his output. The signature is needed to make sure that Bob is sending a legitimate decoding table for decryption. Since Bob verified the opened ones, he is ensured, with good probability, that the e^{th} decoding table is proper.

Simulation-based Proof with Fairness. One important difference between our proof and those of standard 2PC is that in our case the ideal trusted party must only be contacted by the simulator once it is certain that both parties can obtain the output, as first observed by Kılınç and Küpçü [31] for indistinguishability of the ideal and real world outputs. Therefore, to overcome this difficulty, Alice also commits to Bob’s output translation tables as c_i^B using a trapdoor commitment, and opens them for the opened circuits. Bob ensures that the committed and encrypted translation tables are the same (in fact, we encrypt the commitment openings). For the e^{th} circuit, she opens c_e^B at the last step of the protocol. The reason we need these commitments is that, unlike standard covert 2PC, the Alice simulator in the proof for the case of corrupted Bob

does not have $f_B(x_A, x'_B)$ when sending the garbled circuits (since in the fair protocol neither party may learn the output at this stage), and hence cannot embed the output in the e^{th} one at that stage. With trapdoor commitments, at a later stage, she is able to open the translation to something different in order to ensure the “fake” evaluation circuit evaluates to the correct output $f_B(x_A, x'_B)$. The hiding property of the commitment scheme ensures indistinguishability of the simulator’s actions.

Handle Premature Resolutions. The parties have the right to contact the Arbiter. But they may choose to do so at a stage other than the prescribed one. For example, Bob may invoke the output resolution before he sends Alice’s output labels to her. This behavior is mitigated by requiring that Bob provides the Arbiter with Alice’s output labels that match the signed decoding table. Due to output authenticity of the garbling scheme and unforgeability of the signature scheme, Bob cannot cheat against the Arbiter and must provide correct labels. Later on, Alice can recover her output through her own output resolution protocol. A timeout mechanism ensures that Bob must contact the Arbiter during a predefined time⁵, and immediately after that Alice can contact the Arbiter, without waiting indefinitely.

A Note on Synchronicity. Observe that we employ a timeout for resolutions with the Arbiter. Katz et al. [29] define a very nice framework for integrating synchronicity in the Universal Composability [19] framework. They provide a clock functionality which allows all honest parties to proceed further once a particular clock signal is reached, allowing for synchronous protocols. In that setting, they show input completeness and guaranteed termination can be achieved together (though not necessarily fairness). In our protocols, the only place we employ loosely synchronized clocks is for resolutions with the Arbiter. The remaining (optimistic) part of the protocol employs no synchronicity assumptions (just local network timeouts). There are two main reasons we choose to proceed this way: (1) Due to a result by K upc u and Lysyanskaya [41] (see also [40]), if one would like to employ multiple autonomous (independent) entities to replace a single trusted Arbiter, we are forced to employ timing models. (2) Optimistic fair exchange literature shows that the timeout-based resolutions can be exchanged with slightly more expensive protocols (with one more round) that provide fairness without requiring timeouts (see e.g. [5,43]). We believe a similar methodology may be employed here to replace the timeouts, and leave such an extension to our protocols as future work.

Proof overview. We obtain security against malicious Bob as follows: The simulator acts as Alice, except that she commits to and encrypts random values instead of actual output decoding table in c_e^B, d_e^B . Towards the end, if the simulator obtains proper output labels for Alice’s output from the adversarial Bob, then she contacts the ideal trusted party to learn Bob’s output and simulate opening of c_e^B to the actual values. Hiding commitments ensure indistinguishability of Alice’s behavior. If, instead of sending them directly to Alice,

⁵Such timeout mechanisms are easy to implement and standard in the optimistic fair exchange literature (see e.g. [5,43]).

Bob contacts the Arbiter and performs a proper resolution, the simulator simulates the Arbiter, and upon receiving proper output labels for Alice, contacts the ideal trusted party for obtaining Bob’s output. She then sends the corresponding decoding table as if it was the decryption of d_e^B . Semantic security ensures indistinguishability of Alice’s and Arbiter’s behavior.

For security against covert Alice, different from the unfair scenario, the simulator contacts the ideal trusted party if Alice acts properly, and obtains Alice’s output. He sends the corresponding labels back to Alice. He simulates by himself Bob’s Arbiter resolution should Alice not respond back with Bob’s output labels’ openings. If Alice later contacts the Arbiter for resolution, he returns back Alice’s output labels again.

2.2 Fair Malicious 2PC

Security with Abort. Our starting point is the cut-and-choose 2PC of Lindell [45], which contains a *cheating-detection* component to remove the requirement that majority of the circuits are correct, and hence reduce the number of circuits by a factor of 3.

In this protocol, Alice garbles s circuits GC_1, \dots, GC_s with the exception that she uses *the same output labels for all circuits*. Parties also perform ℓ OTs for Bob to learn his input labels. Bob then chooses a random subset of these circuits to be evaluated, and the rest are to be opened and checked for correctness later. Bob evaluates the evaluation circuits. Since output labels are reused for all circuits, Bob expects to retrieve the same labels from all evaluations. If this is indeed the case, he only needs to ensure that one of the evaluations was correct in order to make sure he has the correct output. If Bob obtains different labels for at least a single output wire, he uses the two distinct labels W_0 and W_1 corresponding to values 0 and 1, as his proof of Alice’s cheating.

At this stage, parties engage in a cheating-detection phase, which itself is a malicious cut-and-choose 2PC for evaluating a cheating-detection (CD) circuit. This cut-and-choose is performed using $3s$ circuits (and a majority output), but since the CD circuit is significantly smaller, this will be a small overhead, independent of the actual circuit’s size. The CD circuit takes W_0 and W_1 as Bob’s input (his evidence of Alice’s cheating), and takes Alice’s input x_A to the original computation as her input. If Bob’s two labels are valid proofs (Alice embeds the output labels in the CD circuits, and the CD circuit checks whether Bob’s two labels are among them), he learns Alice’s input x_A and can compute $f(x_A, x_B)$ on his own. Otherwise he learns a random value. It is important that Alice does not know whether Bob learned the output by evaluating the computation circuits or the cheating-detection circuits. Alice then opens the check circuits and Bob aborts if any of the checks fail. Else, he sends Alice’s output labels to her.

Handle Alice’s Input Consistency. Deviating from [45], we handle the consistency of Alice’s inputs using the technique of [60], as it seems more suitable for the tweaks we need to make to input-consistency. In this approach a universal hash function (UH) is evaluated on her input inside the circuits, and Bob verifies that the output of this function is the same in all circuits. Alice’s input is padded

with a short random string r_x in order to increase its entropy and reduce the amount of information that can be learned about the input from the output of the UH. Let ℓ be the input length and s' be a security parameter. [60] shows that a random matrix of dimensions $s' \times (\ell + 2s' + \log s')$ over $GF(2)$ can be used as a UH, where the evaluation consists of multiplying this matrix with the input vector (and getting a vector of length s').

Delay Bob's Output via a One-time Pad. Similar to the covert 2PC, it is easy to see that the above construction is not fair. In particular, Bob can abort the protocol immediately after learning his output and without forwarding Alice's output to her. But unlike our fair covert 2PC, delaying the transmission of the output translation table is *not* sufficient for preventing Bob from learning his output early. Since the same output labels are used for all circuits and a fraction of them are opened, Bob can reconstruct the translation table on his own after the opening phase, and learn his output.

To overcome this issue, we encrypt Bob's output using a one-time pad pad_B that is Alice's additional input to the computation circuit. In particular, the circuit returns $f_B(x_A, x_B) \oplus pad_B$ as Bob's output, and the pad_B itself is only revealed in the final step of the protocol. Alice's output in the circuit is also encrypted using a separate pad pad_A of her choice, to prevent Bob from learning her output even after the opening.

Commit to the Consistent Pad. Note that simply revealing the pad_B to Bob does not provide Bob with sufficient guarantee that it is the same pad_B Alice used in the computation. Hence, for each circuit Alice sends a trapdoor commitment c_i^B to the translation table $PadDec_i$ for the input wires associated with pad_B . She also encrypts the opening of this commitment as d_i^B using the Arbiter's public key, and signs it for resolution purposes. For the opened circuits, c_i^B and d_i^B are opened and checked. In the final stage, in order to reveal pad_B , Alice opens c_i^B for the evaluated circuits. But, we are not done yet. Bob learns one or more pad values used in the evaluation circuits, and needs to determine which is the correct one for decrypting his output. To facilitate this, we apply a separate UH to pad_B (i.e. $M_p \cdot (pad_B || r_p)$) for a random matrix M_p in the computation circuits, which Bob uses in the final stage to determine the "correct" pad among those retrieved. Without this, we could not have guaranteed correctness.

Simulate with Fairness. For simulation purposes, similar to the fair covert 2PC, the fact that the simulator can open c_i^B to an arbitrary pad in the final stage allows the simulation to go through by postponing the query to the ideal trusted party for obtaining the output, until we are sure both parties can learn the output. Remember that such a simulation is a necessity for simulating fair secure computation protocols properly [31].

Commit to Alice's Output Early. Similar to the fair covert 2PC, we also need to ensure that if Alice aborts before revealing pad_B , Bob has enough information to invoke an output resolution protocol with the Arbiter and show evidence that he has been following the steps of the protocol. In the covert protocol, we used the output-authenticity property of the garbling scheme for this purpose, but in the current protocol, output-authenticity is lost after the

opening stage, since all circuits use the *same output labels*. To circumvent this issue, we have Bob commit to the output labels for Alice’s output *before* the opening stage, and have Alice sign the commitment. In case of a resolution, Bob opens the signed commitment for the Arbiter, who checks its correctness and consistency with a signed translation table provided by Alice, and only then decrypts d_i^B escrows for Bob to learn the pads and obtain his output.

Fix Cheating-Detection. Note that in regular cheating-detection, Bob only learns x_A and hence the plaintext version of Alice’s output $f_A(x_A, x_B)$. But, Bob needs to commit to the output labels for Alice’s output, and because the output translation table of Alice corresponds to a padded output, knowing $f_A(x_A, x_B)$ is not sufficient for simulation. Therefore, we need to modify the CD circuit as well. We fix this by having the CD circuit also output pad_A . Bob can now compute $f_A(x_A, x_B) \oplus pad_A$, and use Alice’s output translation table $GDec^A$ to determine which evaluated circuit returned the correct output (we know there is at least one such circuit with all but negligible probability). He commits to those labels as Alice’s output labels.

Proof overview. Our proofs are very similar in essence to the above malicious Bob case. Simulator Alice would commit to and encrypt random values, and later when she obtains the actual output from the ideal trusted party, she would simulate opening them to the correct values. For malicious Alice case, simulator Bob also commits to random labels for Alice’s outputs, and later simulates opening them to proper labels.

3 Preliminaries

Bellare et al. [13] introduce the notion of a **garbling scheme** *Garble* as a cryptographic primitive. Besides the standard privacy guarantees, we heavily take advantage of the *output-authenticity* of a garbling scheme, which intuitively guarantees that the evaluator cannot forge valid output labels except by honestly evaluating the garbled circuit.

In a standard oblivious transfer (OT) protocol [56], the receiver has a selection bit σ , and the sender has two messages a_0, a_1 . At the end of the protocol, the receiver learns a_σ while the sender does not learn anything. In a **committing oblivious transfer** [34,59], at the end of the interaction, the receiver also receives a commitment to the sender’s input messages, and the sender obtains the opening to those commitments. As a result, the receiver can ask the sender to open his messages at a later stage. Efficient constructions for committing OT were proposed in [59] and [52].

The **optimistic fair exchange** literature includes many implementation details we skip here for the sake of clarity (see e.g. [5,42,41]). If Alice already registered her signature verification key with the Arbiter, our protocol can be employed as is. But, if we want anonymity, then the Arbiter must have a way of obtaining this verification key. The standard mechanism is to put it into the label of a labeled encryption scheme. In our case, Alice can generate a new key pair for each computation (or even circuit) and put the verification key into the label of d_i^B encryptions, and Bob can verify the signatures using this verification key. The Arbiter can then also use this key for verification. Details such as these

and how to handle timeouts without tight synchronization are well-discussed in the previous work [5,43,39,38], and hence we do not repeat for the sake of space.

4 Protocols

Due to the page limitations, we defer the security definitions of ideal and real worlds for fair secure two party computation, as well as full security proofs via simulation to the full version [63]. Both protocols remain secure in the unfair sense even if the Arbiter actively cheats and colludes with one of the parties.

In Figures 1 and 2 we provide a full description of our fair covert 2PC, and Figure 7 and 8 show the resolutions with the Arbiter for Bob and Alice, respectively. Figures 3, 4, 5, and 6 describe the full protocol fairly secure against malicious parties, and Figures 9 and 10 show the resolutions with the Arbiter for Bob and Alice, respectively. The vertical lines in the figures represent parts that would not have existed in the unfair counterpart. Tables 2 and 3 summarize the overhead of adding fairness in covert and malicious protocols, respectively.

Extra Rounds	Extra Messages' Size	Operation Type
1	$O(sm)$	Public Key

Table 2. Overhead for fairness (Covert). Round is a single message. s is the statistical security parameter, m is Bob's output length.

Extra Rounds	Extra Input Length	Extra Messages' Size	Operation Type
3	$O(m+t)$	$O(s(m+t))$	Public Key

Table 3. Overhead for fairness (Malicious). Round is a single message. s is the statistical security parameter, m is the output length, t is a security parameter for input consistency.

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Alice's input: $x_A \in \{0, 1\}^\ell$. **Bob's input:** $x_B \in \{0, 1\}^\ell$.

Common input: Alice and Bob agree on the description of a circuit C , where $C(x_A, x_B) = f(x_A, x_B) = (f_A(x_A, x_B), f_B(x_A, x_B))$, and a second-preimage resistant hash function $H : \{0, 1\}^* \rightarrow \{0, 1\}^\ell$.

s is a statistical security parameter that is inversely proportional to the bound on the cheating probability. L is a computational security parameter, so, for example, each key label is L -bits long. Let $\text{TCommit}(\cdot)$ be a trapdoor commitment scheme.

Setup: Let (PK_T, SK_T) be the Arbiter's key pair for a public key encryption, and (SK_A, VK_A) be the signing-verification key-pair for a digital signature scheme for Alice. At the beginning of the protocol, both parties obtain the Arbiter's public key from the Arbiter. Alice sends her verification key to Bob and the Arbiter.

Output: Alice learns an m -bit string $f_A(x_A, x_B)$ and Bob learns an m -bit string $f_B(x_A, x_B)$.

Alice Prepares the Garbled Circuits.

1. For $1 \leq i \leq s$, Alice computes $\text{GC}_i \leftarrow \text{Garble}(C)$.
2. Let $\text{in}_b^{A,i,j}$ denote the key for bit b for Alice's j^{th} input wire in the i^{th} garbled circuit for $b \in \{0, 1\}$, $1 \leq i \leq s$, and $1 \leq j \leq \ell$. $\text{in}_b^{B,i,j}$ is defined similarly for Bob's input labels.
3. Let $\text{out}_b^{A,i,j}$ denote the key for bit b for Alice's j^{th} output wire in the i^{th} garbled circuit. $\text{out}_b^{B,i,j}$ is used for Bob's output labels.
4. Alice lets $\text{GDec}_i^B = \{\text{out}_0^{B,i,j}, \text{out}_1^{B,i,j}\}_{j=1}^m$ be the decoding table for Bob's output.
5. Alice computes $\text{GDec}_i^A = \{H(\text{out}_{b_{i,j}}^{A,i,j}), H(\text{out}_{-b_{i,j}}^{A,i,j})\}_{j=1}^m$ for random bits $b_{i,j}$ as the output validity-checking table for her output. [This table is randomly permuted based on the bits $b_{i,j}$ such that in case of output resolution, the Arbiter can check validity of output labels without learning their actual value.]
6. She computes $c_i^B = \text{TCommit}(\text{GDec}_i^B)$ as the commitment to Bob's output decoding table. Let GDecOpen_i^B be the opening of this commitment. She encrypts this opening as $d_i^B = E_{PK_T}(\text{GDecOpen}_i^B)$ using the Arbiter's public key. [GDec_i^A and d_i^B will be used by the Arbiter to verify Bob's honesty, and give back Bob's output decoding table, respectively. c_i^B will be employed by Bob to ensure that Alice behaves honestly at the last step. Note that GDec_i^A is computed using a second-preimage resistant hash function, whereas c_i^B is computed via trapdoor commitments.]
7. Alice signs those as $\sigma_i = \text{Sign}(SK_A, (sid, \text{GDec}_i^A, d_i^B))$. [This signature will tie the two decryption tables to the same circuit, and be checked by the Arbiter. sid is the unique session identifier.]

Oblivious Transfer for Bob's Input.

1. Alice and Bob engage in ℓ committed OTs, where in the j^{th} OT, Bob's input is $x_{B,j}$ and Alice input is a pair where the first component is $[\text{in}_0^{B,1,j}, \dots, \text{in}_0^{B,s,j}]$ and the second component is $[\text{in}_1^{B,1,j}, \dots, \text{in}_1^{B,s,j}]$. As a result, Bob learns $\text{in}_{x_{B,j}}^{B,i,j}$ for $1 \leq i \leq s$, $1 \leq j \leq \ell$.

Alice Sends the Circuits.

1. Alice sends $\{\text{GC}_i, \text{GDec}_i^A, c_i^B, d_i^B\}_{i=1}^s$ to Bob.

Fig. 1. Optimistic Fair Covert 2PC

OT-based Challenge Generation.

1. Bob picks a random challenge index e , lets $b_e = 0$, and $b_i = 1$ for all $i \neq e$.
2. Alice and Bob run s OTs where Bob's input (as the receiver) in the i^{th} OT is b_i while Alice (as the sender) inputs a pair where the first component is her garbled inputs $\{\text{in}_{X_{A,j}}^{A,i,j}\}_{j=1}^{\ell}$ along with σ_i , and the second component is the openings for $\text{GC}_i, \text{GDec}_i^A, c_i^B, d_i^B$ and the input and randomness she used in the i^{th} committed OT above. In other words, for $i \neq e$ Bob learns openings for everything about the circuits, and for $i = e$ he learns Alice's input labels and her signature.

Bob Verifies Check Circuits.

1. For $i \neq e$, Bob uses the openings he obtained in the challenge generation phase to check the correctness of GC_i . He verifies the consistency of c_i^B with d_i^B both of which he has openings for. He checks that GDec_i^B is consistent with GC_i .
2. For $i = e$, he verifies the signature σ_e .

Bob Evaluates.

1. Note that Bob has Alice's input labels for the e^{th} circuit via the OT-based challenge generation and his own input labels via the committed OT. He evaluates GC_e .

Output Exchange.

1. Bob tells Alice that he is done with the evaluation.
2. Alice responds by sending $\sigma_t = \text{Sign}(SK_A, (\text{sid}, \text{deadline}))$. Bob checks the timeout and the session identifier are consistent with the agreed upon values, and aborts otherwise.
3. Denote the labels for Alice's output by $\{\text{out}_{\text{out}_{A,j}}^{A,e,j}\}_{j=1}^m$. Bob sends these to Alice, along with σ_e so that Alice will learn the evaluated circuit identifier e , and then she translates them to her actual output on her own. If Alice does not receive the correct labels in time, she contacts the Arbiter for resolution.
4. Alice opens c_e^B to the decoding table GDec_e^B , which Bob uses to decode his actual output. If Bob does not receive the correct decoding table in time, he contacts the Arbiter for resolution.

Fig. 2. Optimistic Fair **Covert** 2PC (cnt'd)

Alice's input: $x_A \in \{0, 1\}^{\ell}$. **Bob's input:** $x_B \in \{0, 1\}^{\ell}$.

Common input: Alice and Bob agree on the description of a circuit C , where $C(x_A, x_B) = f(x_A, x_B) = (f_A(x_A, x_B), f_B(x_A, x_B))$, and a second-preimage resistant hash function $H : \{0, 1\}^* \rightarrow \{0, 1\}^{\ell}$.

s is a statistical security parameter that represents the bound on the cheating probability. L is a computational security parameter, so, for example, each key label is L -bits long. s' is a statistical security parameter associated with the input-consistency matrix. Let $t = 2s' + \log s'$, and $\ell' = 2m + \ell + 2t$.

Let $\text{TCommit}(\cdot)$ be a trapdoor commitment scheme, and $\text{Commit}(\cdot)$ be a regular commitment scheme.

Fig. 3. Optimistic Fair **Malicious** 2PC Inputs

Setup: Let (PK_T, SK_T) be the Arbiter's key pair for a public key encryption, and (SK_A, VK_A) be the signing-verification key-pair for a digital signature scheme for Alice. At the beginning of the protocol, both parties obtain the commitment parameters, and the Arbiter's public key from the Arbiter. Alice sends her verification key to Bob and the Arbiter.

Output: Alice learns an m -bit string $out_A = f_A(x_A, x_B)$ and Bob learns an m -bit string $out_B = f_B(x_A, x_B)$.

Alice Prepares Input/Output Labels.

1. Alice chooses s PRF seeds sd_1^A, \dots, sd_s^A , and commits to them using $\text{Commit}(sd_1^A), \dots, \text{Commit}(sd_s^A)$. All the randomness Alice will use for generating the i^{th} garbled computation circuit and its input labels will be derived from sd_i^A . Similarly, she chooses $3s$ PRF seeds $sd'_1{}^A, \dots, sd'_{3s}{}^A$, and commits to them, where the randomness she uses for generating the i^{th} garbled cheating-detection (CD) circuit and its input labels will be derived from $sd'_i{}^A$.
2. Alice chooses $r_x, r_p \in_R \{0, 1\}^t, pad_A, pad_B \in_R \{0, 1\}^m$ and sets $x_A^C = pad_B \| r_p \| x_A \| pad_A \| r_x$. She will be using x_A^C as her input to the computation circuits instead of x_A . We denote the j^{th} bit of x_A^C by $x_{A,j}^C$.
3. Alice chooses $in_b^{A,i,j} \in_R \{0, 1\}^L$ for $b \in \{0, 1\}, 1 \leq i \leq s$ and $1 \leq j \leq \ell'$. $in_b^{A,i,j}$ would be the b -key for Alice's j^{th} input wire in the i^{th} computation garbled circuit.
4. Alice sends $\text{Commit}(H(in_{x_{A,1}^C}^{A,i,1}} \| \dots \| in_{x_{A,\ell'}^C}^{A,i,\ell'}))$ for $1 \leq i \leq s$, i.e. commitments to encoding of her inputs. This is intended to commit Alice to her inputs before the matrices associated with input-consistency are chosen.
5. Alice lets her input to the CD circuit be $x_A^{CD} = x_A \| pad_A \| r_x$. She chooses random labels for the associated input wires in the CD circuits and commits to the encoding of her inputs as she did for the computation circuits.
6. Alice chooses $W_b^{A,j} \in_R \{0, 1\}^L$ for $j \in \{1, \dots, m\}$ and $b \in \{0, 1\}$. Similarly, she chooses $W_b^{B,j} \in \{0, 1\}^L$. These correspond to labels for output wires corresponding to Alice's and Bob's output (padded with Alice's pads), respectively, and unlike the covert protocol, will be the *same* across all s circuits.
7. Alice lets $\text{GDec}^B = \{H(W_0^{B,j}), H(W_1^{B,j})\}_{j=1}^m$ be the decoding table for Bob's output. She also lets $\text{GDec}^A = \{H(W_0^{A,j}), H(W_1^{A,j})\}_{j=1}^m$. The translation table for other outputs of the circuit (i.e. outputs of the UH functions) will be created in the standard way and with different labels for each circuit.
8. Alice lets $\text{PadDec}_i = \{in_0^{A,i,j}, in_1^{A,i,j}\}_{j=1}^{m+t}$ for the i^{th} circuit (note that the first $m+t$ input wires are associated with Alice's pad_B and r_p). This is essentially a decoding table for input wires for Alice's pad_B and r_p . Alice then commits to this table $c_i^B = \text{TCommit}(\text{PadDec}_i)$ using the trapdoor commitment scheme, and encrypts its opening as $d_i^B = E_{PK_T}(\text{PadDecOpen}_i)$ using the Arbiter's public key.

Fig. 4. Optimistic Fair Malicious 2PC

Alice Prepares the Garbled Circuits.

1. Alice and Bob jointly choose random binary matrices $M_x \in_R \{0,1\}^{s' \times \ell + m + t}$, $M_p \in_R \{0,1\}^{s' \times m + t}$. Let $C'(x_A^C, x_B) = (f_A(x_A, x_B) \oplus pad_A, (f_B(x_A, x_B) \oplus pad_B, M_x \cdot (x_A || pad_A || r_x), M_p \cdot (pad_B || r_p)))$. In other words, the circuit pads Alice and Bob's output with separate pads generated by Alice, and also outputs the result of applying the M_x and M_p to x_A and pad_B for input-consistency checks.
2. For $1 \leq i \leq s$, Alice computes $GC_i \leftarrow \text{Garble}(C')$ with the consideration that she uses the input and output labels she generated above for the garbling.
3. Denote by CD the cheating detection circuit. Alice's input to this circuit is $x_A^{CD} = x_A || pad_A || r_x$. Bob's input is an L -bit string pc , his (potential) proof of Alice's cheating. CD 's computation is as outlined in Lindell [45] with the exception that in case of detected cheating x_A and pad_A are both revealed to Bob. In particular, CD has the labels $\{W_0^{B,j}, W_1^{B,j}\}_{j=1}^m$ and $\{W_0^{A,j}, W_1^{A,j}\}_{j=1}^m$ embedded in it and checks whether pc is the XOR of the 0-key and the 1-key for any of the wires. If so, it outputs to Bob $x_A || pad_A$. Otherwise, it outputs a random string. CD also outputs $M_x \cdot (x_A || pad_A || r_x)$ to Bob. Alice has no output.
4. For $1 \leq i \leq 3s$, Alice computes $GCD_i \leftarrow \text{Garble}(CD)$ with the consideration that she uses the input labels she generated above for garbling. The translation tables for GCD_i are generated in the standard way.

Oblivious Transfer for Bob's Input to Computation Circuits. Alice and Bob engage in ℓ committed OTs, where in the j^{th} OT, Bob's input is $x_{B,j}$ and Alice's input is a pair where the first component is $[\text{in}_0^{B,1,j}, \dots, \text{in}_0^{B,s,j}]$ and the second component is $[\text{in}_1^{B,1,j}, \dots, \text{in}_1^{B,s,j}]$. As a result, Bob learns $\text{in}_{x_{B,j}}^{B,i,j}$ for $1 \leq i \leq s, 1 \leq j \leq \ell$.

Alice Sends the Garbled Circuits. Alice sends $\{GC_i, c_i^B, d_i^B\}_{i=1}^s$ and GDec^A , GDec^B , and $\sigma_{\text{out}^A} = \text{Sign}(SK_A, (\text{sid}, \text{GDec}^A))$ to Bob (where sid is the unique session identifier). She also sends $\{GCD_i\}_{i=1}^{3s}$ and the associated output translation tables.

Challenge Generation. Alice and Bob jointly run a simulatable coin-toss to generate a uniformly random s -bit string b and a uniformly random $3s$ -bit string b' . Define the evaluation set \mathbb{E} where $i \in \mathbb{E}$ if and only if $b_i = 0$, and the evaluation set \mathbb{E}' similarly with respect to b' . Both parties learn \mathbb{E} and \mathbb{E}' . Circuits are *not* opened immediately, though.

Bob Evaluates Computation Circuits in \mathbb{E} .

1. Alice sends her garbled input labels for x_A^C for all $GC_i, i \in \mathbb{E}$, by opening the commitments she made to them earlier. Alice also sends $\sigma_{i,pad} = \text{Sign}(SK_A, (\text{sid}, d_i^B))$ for $i \in \mathbb{E}$. Bob uses these input labels and those of his own from the committed OTs to evaluate all GC_i where $i \in \mathbb{E}$.
2. If there is at least one circuit with a valid output, and all circuits with a valid output return the same output labels $W_{o_A,1}^{A,1}, \dots, W_{o_A,m}^{A,m}$ and $W_{o_B,1}^{B,1}, \dots, W_{o_B,m}^{B,m}$, Bob lets $C_A = \text{TCommit}(W_{o_A,1}^{A,1}, \dots, W_{o_A,m}^{A,m})$. Note that through these, Bob can learn $o_A = \text{out}_A \oplus pad_A$ and $o_B = \text{out}_B \oplus pad_B$, but since he does not know the pads, these are useless. Bob lets pc be a random L -bit string.
3. If there are at least two circuits with valid but different outputs, Bob chooses the first output wire with different labels and denotes the two labels by W and W' . $pc = W \oplus W'$ will constitute Bob's input to the cheating detection circuits.
4. If all circuits are evaluated to *invalid* output labels (i.e. the obtained labels are not consistent with GDec^A and GDec^B) or if the output of the UHs in any two circuits are different Bob does not abort (until after the opening stage) but instead commits to a random string of appropriate length in C_A .

Fig. 5. Optimistic Fair Malicious 2PC (cnt'd)

Evaluating Cheating-Detection Circuits in \mathbb{E}' .

1. Alice and Bob engage in L committed OTs, where in the j^{th} OT, Bob's input is pc_j and Alice's input is a pair where the first component is the 3s input labels corresponding to 0 and the second component is the 3s labels corresponding to 1.
2. Alice sends her garbled input labels for x_A^{CD} for GCD_i where $i \in \mathbb{E}'$, by opening the commitments she made to them earlier.
3. Bob uses the input labels to evaluate all GCD_i with $i \in \mathbb{E}'$, and uses the translation tables to translate to plaintext outputs. If any two UH outputs are different or if they are different from those output in the computation circuits Bob postpones aborting until the opening stage, commits to a random string of appropriate length for C_A .
4. Else, he considers the majority output as the correct output. If Bob had a valid proof of cheating pc , he learns $x_A \parallel pad_A$. He computes $o_A = f_A(x_A, x_B) \oplus pad_A$ on his own. He then chooses a $\{W_{o_A, j}^{A, j}\}_{j=1}^m$ from the evaluation circuits that is consistent with GDec^A and o_A , and lets $C_A = \text{TCommit}(W_{o_A, 1}^{A, 1}, \dots, W_{o_A, m}^{A, m})$ (with high probability there is at least one). [Note that in case Alice's opening of the check circuits are problematic, Bob will never decommit anyways.]

Bob Commits to Alice's Garbled Output.

1. Bob sends C_A as his commitment to Alice's output labels.
2. Alice sends back $\sigma_{o_A} = \text{Sign}(SK_A, (sid, deadline, C_A))$. Bob can use this in case of resolution to prove to the Arbiter that he computed Alice's output honestly.

Alice Opens Everything for Check Circuits.

1. For $i \notin \mathbb{E}$, Alice opens sd_i^A to open all secrets of GC_i . She also opens c_i^B, d_i^B , and the randomness used in committed OTs for Bob's input. Bob checks correctness of opened circuits and their consistency with $\text{GDec}^A, \text{GDec}^B$. He also verifies correctness c_i^B, d_i^B and the opened PadDec_i . He aborts if any of the checks fail.
2. For $i \in \mathbb{E}'$, Alice opens sd_i^A to open all secrets of GCD_i . He also reveals the randomness used in committed OTs for Bob's input. Bob checks the correctness of the opened circuits and the OTs, and aborts in case of a fail.

Output Exchange.

1. Bob opens C_A to Alice's output labels. Alice translates these to her actual output out_A using pad_A and the translation table, on her own. In case of a problem, Alice resolves with the Arbiter.
2. Alice opens c_i^B for $i \in \mathbb{E}$. This allows Bob to learn the values Alice used for pad_B, r_p in all evaluated circuits. For each such value he computes $M_p \cdot (pad_B \parallel r_p)$ and checks if the result is equal to the unique UH output he obtained when evaluating the circuits. He chooses a pad meeting this requirement and uses it to decode his final output out_B . In case of a problem, Bob resolves with the Arbiter.

Fig. 6. Optimistic Fair Malicious 2PC (cnt'd)

1. Bob sends $\text{GDec}_e^A, d_e^B, \sigma_e, \sigma_t$ to the Arbiter. He also sends labels for Alice's output i.e. $\{\text{out}_{out_{A,j}}^{A,e,j}\}_{j=1}^m$.
2. The Arbiter verifies the signatures, checks that the time is earlier than the deadline in σ_t and the session identifiers are matching. He also makes sure $\text{out}_{out_{A,j}}^{A,e,j}$ values are consistent with GDec_e^A . Essentially, one output label per pair must be provided. He aborts if any of the checks fail.
3. In case of no fails, the Arbiter decrypts d_e^B and sends GDecOpen_e^B to Bob. He stores $\{\text{out}_{out_{A,j}}^{A,e,j}\}_{j=1}^m$ for Alice.
4. Bob checks that GDecOpen_e^B is the correct opening for $c_e^B,^a$ and uses GDec_e^B in the opening to translate his output labels to actual outputs.

^aThis check is necessary against potentially malicious Arbiter to preserve correctness.

Fig. 7. Resolution for Bob (for Optimistic Fair **Covert** 2PC)

1. If Alice contacts the Arbiter *before* the timeout and Bob has *not* contacted the Arbiter yet, the Arbiter tells Alice to come after the timeout.
2. If Alice contacts the Arbiter *after* the timeout and Bob has *not* contacted the Arbiter yet, the protocol is aborted and no party obtains the actual output.
3. Else (Bob already contacted the Arbiter and resolved), the Arbiter sends $\{\text{out}_{out_{A,j}}^{A,e,j}\}_{j=1}^m$ obtained via Bob's resolution to Alice (after making sure she is the same Alice, e.g. by asking for the input to a one way function whose output was in the associated signature given by Bob, see e.g. [5]).
4. Alice translates $\{\text{out}_{out_{A,j}}^{A,e,j}\}_{j=1}^m$ to actual outputs on her own.

Fig. 8. Resolution for Alice (for Optimistic Fair **Covert** 2PC)

1. Bob sends $\text{GDec}_e^A, \sigma_{\text{GDec}_e^A}, C_A, \sigma_{o_A}$ to the Arbiter. He also sends $d_i^B, \sigma_{i,pad}$ for all $i \in \mathbb{E}$ to the Arbiter. He also opens C_A to $W_{o_{A,1}}^{A,j}, \dots, W_{o_{A,m}}^{A,j}$.
2. The Arbiter verifies the signature, checks that the time is earlier than the deadline in the signature and the session identifiers match. He also makes sure the opened values $W_{o_{A,1}}^{A,j}, \dots, W_{o_{A,m}}^{A,j}$ are consistent with C_A and GDec_e^A . Essentially, one output label per pair must be provided. He aborts if any of the checks fail.
3. In case of no fails, the Arbiter decrypts d_i^B for $i \in \mathbb{E}$ and sends PadDecOpen_i to Bob. He stores $W_{o_{A,1}}^{A,1}, \dots, W_{o_{A,m}}^{A,m}$ for Alice.
4. Bob checks that PadDecOpen_i^B is the correct opening for c_i^B , for $i \in \mathbb{E}$, and then uses PadDec_i values to obtain his actual output out_B as in the last step of the main protocol.

Fig. 9. Resolution for Bob (for Optimistic Fair **Malicious** 2PC)

1. If Alice contacts the Arbiter *before* the timeout and Bob has *not* contacted the Arbiter yet, the Arbiter tells Alice to come after the timeout.
2. If Alice contacts the Arbiter *after* the timeout and Bob has *not* contacted the Arbiter yet, the protocol is aborted and no party obtains the actual output.
3. Else (Bob already contacted the Arbiter and resolved), the Arbiter sends $W_{o_{A,1}}^{A,1}, \dots, W_{o_{A,m}}^{A,m}$ obtained via Bob's resolution to Alice.
4. Alice translates $W_{o_{A,1}}^{A,1}, \dots, W_{o_{A,m}}^{A,m}$ to her actual outputs on her own.

Fig. 10. Resolution for Alice (for Optimistic Fair **Malicious** 2PC)

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